

What is claimed is:

Sub A1

1. An information processing device comprising:
a display device having flat panel displays for right
and left eyes mounted on the head of a user;
a controller connected to said display device; and
an input operation device connected to said controller,
wherein said flat panel displays are capable of displaying a
plurality of pieces of information at a time.

Sub A2

2. A device according to claim 1, wherein channel formation
regions of TFTs connected to pixel electrodes of the flat
panel displays of said display device are constituted by a
semiconductor thin film formed by a collection of a
plurality of bar-shaped or planar bar-shaped crystals formed
on an insulating surface.

Sub A2

3. A device according to claim 2, wherein the plane of said
channel formation regions is oriented substantially in a
 $\{110\}$ -direction.

Sub C1

4. A device according to claim 2, wherein 90 % or more of
crystal lattices at grain boundaries of said channel
formation regions have continuity.

5. A device according to claim 1, wherein said flat panel
displays comprise a display device on which one screen is
written at frequencies in the range from 30 Hz to 180 Hz and
on which screen display is carried out with the polarity of
the voltage applied to the pixel electrodes inverted for
each screen.

6. A device according to claim 1, wherein said flat panel

Sub A1

displays comprise a display device which is a liquid crystal display using a liquid crystal material which is antiferroelectric liquid crystals or ferroelectric liquid crystals substantially having no threshold.

Sub A2

7. An information processing device comprising:
a display device having flat panel displays for right and left eyes mounted on the head of a user;

a controller; and
an input operation device connected to said controller, wherein said controller transmits a signal in the form of an electric wave to said display device and wherein said flat panel displays are capable of displaying a plurality of pieces of information at a time.

Sub A3

8. A device according to claim 7, wherein channel formation regions of TFTs connected to pixel electrodes of the flat panel displays of said display device are constituted by a semiconductor thin film formed by a collection of a plurality of bar-shaped or planar bar-shaped crystals formed on an insulating surface.

Sub A4

9. A device according to claim 8, wherein the plane of said channel formation regions is oriented substantially in a {110}-direction.

Sub A5

10. A device according to claim 8, wherein 90 % or more of crystal lattices at grain boundaries of said channel formation regions have continuity.

Sub A6

11. A device according to claim 7, wherein said flat panel displays comprise a display device on which one screen is

written at frequencies in the range from 30 Hz to 180 Hz and on which screen display is carried out with the polarity of the voltage applied to the pixel electrodes inverted for each screen.

12. A device according to claim 7, wherein said flat panel displays comprise a display device which is a liquid crystal display using a liquid crystal material which is antiferroelectric liquid crystals or ferroelectric liquid crystals substantially having no threshold.

13. An information processing device comprising:
a display device having flat panel displays for right and left eyes mounted on a head of a user;
a controller;
an image pick-up device; and
an input operation device connected to said controller, wherein said controller transmits a signal in the form of an electric wave to said display device, wherein said image pick-up device converts at least images of said input operation device and a hand of said user into electrical signals and supplies said electrical signals to said controller and wherein said flat panel displays display a plurality of pieces of information including at least said images of the input operation device and said hand of the user at a time.

14. A device according to claim 13, wherein channel formation regions of TFTs connected to pixel electrodes of the flat panel displays of said display device are constituted by a semiconductor thin film formed by a collection of a plurality of bar-shaped or planar bar-shaped

Sub El crystals formed on an insulating surface.

Sub Al

15. A device according to claim 14, wherein the plane of said channel formation regions is oriented substantially in a $\{110\}$ -direction.

Sub S

16. A device according to claim 14, wherein 90 % or more of crystal lattices at grain boundaries of said channel formation regions have continuity.

Sub S

17. A device according to claim 13, wherein said flat panel displays comprise a display device on which one screen is written at frequencies in the range from 30 Hz to 180 Hz and on which screen display is carried out with the polarity of the voltage applied to the pixel electrodes inverted for each screen.

Sub S

18. A device according to claim 13, wherein said flat panel displays comprise a display device which is a liquid crystal display using a liquid crystal material which is antiferroelectric liquid crystals or ferroelectric liquid crystals substantially having no threshold.